July 2005

KSD1621 NPN Epitaxial Silicon Transistor



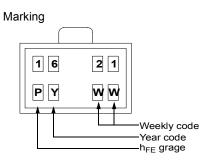
SEMICONDUCTOR®

KSD1621 NPN Epitaxial Silicon Transistor

High Current Driver Applications

- Low Collector-Emitter Saturation Voltage
- Large Current Capacity and Wide SOA
- Fast Switching Speed
- Complement to KSB1121





Absolute Maximum Ratings T_a = 25°C unless otherwise noted

Symbol	Parameter	Ratings	Units
V _{CBO}	Collector-Base Voltage	30	V
V _{CEO}	Collector-Emitter Voltage	25	V
V _{EBO}	Emitter-Base Voltage	6	V
I _C	Collector Current	2	A
P _C Collector Power Dissipation P _C *		500 1.3	mW W
TJ	Junction Temperature	150	°C
T _{STG}	Storage Temperature	-55 ~ 150	°C

Mounted on Ceramic Board (250mm² x 0.8mm)

Electrical Characteristics T_a = 25°C unless otherwise noted

Symbol	Parameter	Test Condition	Min.	Тур.	Max.	Units
BV _{CBO}	Collector-Base Breakdown Voltage	$I_{\rm C} = 10 \mu {\rm A}, I_{\rm E} = 0$	30			V
BV _{CEO}	Collector-Emitter Breakdown Voltage	I _C = 1mA, I _B = 0	25			V
BV _{EBO}	Emitter-Base Breakdown Voltage	I _E = 10μA, I _C = 0	6			V
I _{CBO}	Collector Cut-off Current	V _{CB} = 20V, I _E = 0			100	nA
I _{EBO}	Emitter Cut-off Current	V _{BE} = 4V, I _C = 0			100	nA
h _{FE1} h _{FE2}	DC Current Gain	$V_{CE} = 2V, I_C = 0.1A$ $V_{CE} = 2V, I_C = 1.5A$	100 65		560	
V _{CE} (sat)	Collector-Emitter Saturation Voltage	I _C = 1.5A, I _B = 75mA		0.18	0.4	V
V _{BE} (sat)	Base-Emitter Saturation Voltage	I _C = 1.5A, I _B = 75mA		0.85	1.2	V

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Electrical Characteristics (continued) T_a = 25°C unless otherwise noted

Symbol	Parameter	Test Condition	Min.	Тур.	Max.	Units
f _T	Current Gain Bandwidth product	V _{CE} = 10V, I _C = 50mA		150		MHz
C _{ob}	Output Capacitance	V _{CB} = 10V, I _E = 0, f = 1MHz		19		pF
t _{ON}	Turn On Time *	V _{CC} = 12V, V _{BE} = 5V		60		ns
t _{STG}	Storage Time *	$I_{B1} = -I_{B2} = 25 \text{mA}$ $I_C = 0.5 \text{A}, \text{ R}_1 = 25 \Omega$		500		ns
t _F	Fall Time *			25		ns

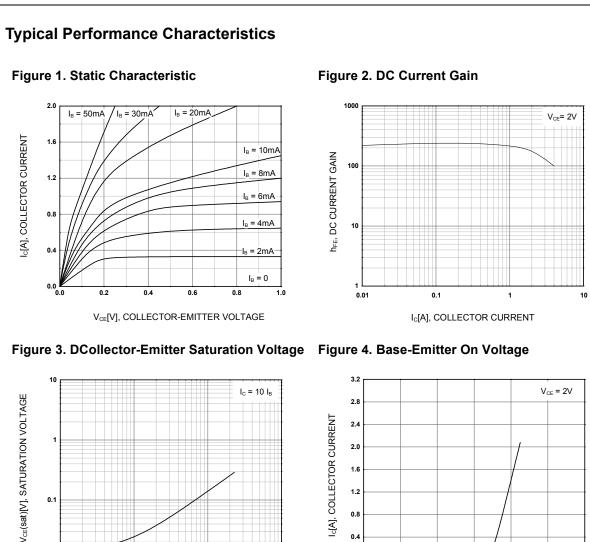
h_{FE} Classification

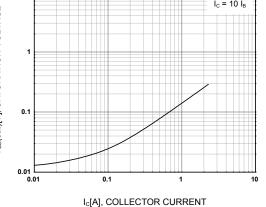
Classification	R	S	т	U
h _{FE}	100 ~ 200	140 ~ 280	200 ~ 400	280 ~ 560

Package Marking and Ordering Information

Device Marking	Device	Package	Reel Size	Tape Width	Quantity
1621	KSD1621	SOT-89	13"		4,000









0.2

1.6 1.2

0.8 0.4 0.0 L 0.0

Figure 5. Collector Output Capacitance

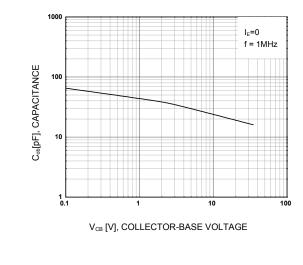


Figure 6. Current Gain Bandwidth Product

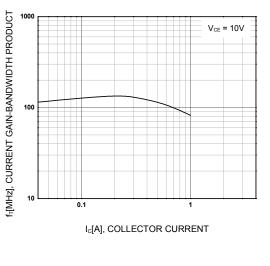
0.4

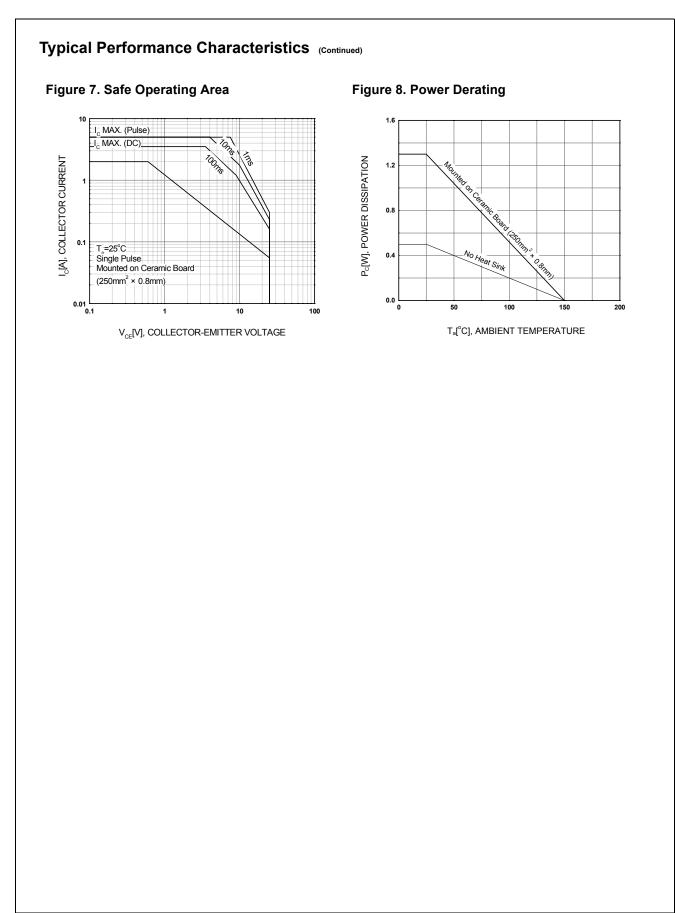
0.6

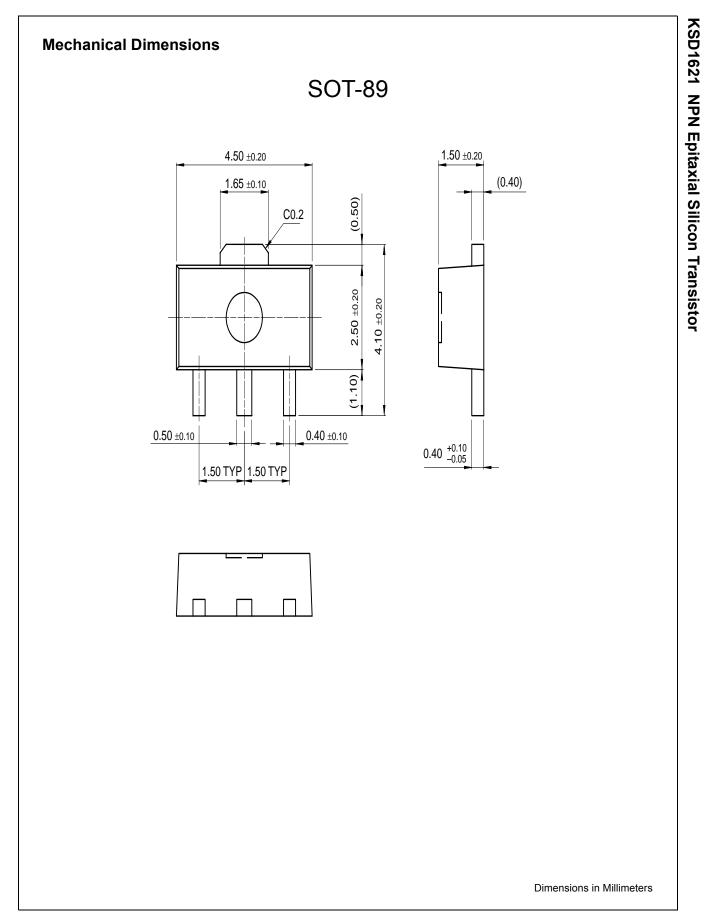
0.8

1.0

1.2







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